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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Agarwal et al.

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For: METHODS FOR FORMING AND
INTEGRATED CIRCUIT STRUCTURES
CONTAINING RUTHENIUM AND
TUNGSTEN CONTAINING LAYERS

Examiner: Cuong Q. Nguyen

Art Unit: 2811

Attorney Reference No. 6047-53173

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

Attorney
for Applicant(s) 

Date Mailed July 22, 2004

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)(3)**

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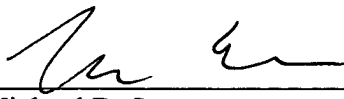
This Information Disclosure Statement and the enclosed reference listed on the attached Form PTO-1449 are being filed to comply with the Applicants' duty of disclosure.

Applicants are filing this Information Disclosure Statement ("IDS") before the mailing date of a first Office action on the merits. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A duplicate copy of this Information Disclosure Statement is enclosed.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

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